

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): A. Satoh et al.
Docket No.: JP920020242US1
Serial No.: 10/762,174
Filing Date: January 21, 2004
Group: 2193
Examiner: Tan V. Mai

Title: Multiplier and Cipher Circuit

AMENDMENT AFTER FILING OF APPEAL UNDER 37 C.F.R. §41.33(b)

Commissioner for Patents
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Sir:

In response to the Examiner's Answer dated April 29, 2008, Applicants amend claim 4 pursuant to M.P.E.P. §41.33(b) as follows: